ADITYA INSTITUTE OF TECHNOLOGY AND MANAGEMENT (AITAM) TEKKALI

(Approved by AICTE, Accredited by NBA Affiliated to JNTU, Kakinada)

STUDENT ATTENDENCE REGISTER

Name	: MATTA MANI SANKAR
Subject	: STLD (EEE Dept.)
Class	: II-B
Year	: 2014-15

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LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Act Upon Revie
1	1/9/14	Introduction to Digital Electronics	1	CR		
2	1/9/14	Review of whole course	1	CR		
3	2/9/14	Review of No. system	1	CR		
4	8/9/14	Conversion b/w bases	1	CR		
5	8/9	Conversion b/w bases	1	CR		
6	10/9	1's & 2's compliment	1	CR		
7	11/9	Codes - BCD, XS-3	1	CR		
8	15/9	Codes - Gray code, error detection	1	CR		
9	17/9	Codes - Error detection & correction	1	CR		
10	18/9	Codes - Parity checking & Hamming code	1	CR		
11	22/9	Logic Gates NOT, AND, OR, NAND, NOR	2	CR		
12	24/9	Universal gates, Boolean theorems	2	CR		
13	25/9	Minimization of logic functions using theorems	2	"		
14	26/9	Minimization of logic functions using theorems	2	"		
15	29/9	SOP & POS	2	"		
16	30/9	SOP & POS	2	"		
17	6/10	Multilevel NAND-NAND	2	"		
18	8/10	Multilevel NOR-NOR	2	"		
19	27/10	K-map - 2 variables	3	"		
20	29/10	K-map - 3 variables	3	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
21	30/10/14	K-Map by variably	3	CR		
22	31/10/14	K-Map 4 variables & 5 variables	3	CR		
23	3/11/14	Code converters	3	"		
24	5/11/14	Code converters	3	"		
25	6/11/14	Tabular minimization	3	"		
26	7/11/14	Tabular minimization	3	"		
27	10/11/14	Binary multiplier	3	"		
28	12/11/14	Half adder, Full adder	4	"		
29	13/11/14	Half sub, Full sub	4	"		
30	14/11/14	4-bit adder	4	"		
31	17/11/14	4-bit subtractor applications of adders	4	"		
32	19/11	Look-ahead adder	4	"		
33	20/11	EX-3 adders	4	"		
34	21/11	Adder-subtractor ckt.	4	"		
35	24/11	Encoder & Decoder	4	"		
36	26/11	Mux & Demux	4	"		
37	27/11	7-segment display	4	"		
38	28/11	Comparator, priority encoder	4	"		
39	1/12	Combinational design using mux	4	"		
40	3/12	Latch, Flip flop	5	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
41.	4/12	SRFF, JKFF	5	CR		
42.	5/12	JKFF, Masterslave	5	"		
43.	8/12	FF conversions	5	"		
44.	10/12	Counters - Ripple	5	"		
45.	11/12	Counters - Synchronous	5	"		
46.	14/12	Johnson counters	5	"		
47.	15/12	Ring counters	5	"		
48.	17/12	Registers - Buffer	5	"		
49.	18/12	Control Buffer register	5	"		
50.	19/12	Shift register	5	"		
51.	21/12	Bidirectional shift register	5	"		
52.	23/12	Universal shift register	5	"		

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