LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	19-1-15	Introduction to DSP	I	Black board		
2	19-1-15	Classification of Signals & systems	"	"		
3	20-1-15	Classification of signals & systems	"	"		
4	22-1-15	Discrete time Signals & sequences	"	"		
5	23-1-15	Linear shift invariant systems	"	"		
6	27-1-15	Stability	"	"		
7	29-1-15	Causality	"	"		
8	30-1-15	Linear constant coeff difference equations	"	"		
9	2-2-15	Freq domain representation of discrete time signals	"	"		
10	2-2-15	Freq domain representation of discrete time systems	"	"		
11	3-2-15	Properties of DFS	II	"		
12	5-2-15	Properties of DFS	"	"		
13	6-2-15	DFS representation of Periodic sequences	"	"		
14	9-2-15	DFS representation of periodic sequences	"	"		
15	9-2-15	Properties of DFT	"	"		
16	10-2-15	Properties of DFT	"	"		
17	12-2-15	Linear Conv of sequences using DFT	"	"		
18	13-2-15	Linear Conv of sequences using DFT	"	"		
19	16-2-15	Computation of DFT	"	"		
20	16-2-15	Relation blw Z.T & DFS.	"	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
21	17-2-15	FFT- Radix-2 DITFFT	III	Black board		
22	19-2-15	Radix-2 DITFFT	"	"		
23	20-2-15	Radix-2 DITFFT	"	"		
24	23-2-15	Radix-2 DIFFFT	"	"		
25	23-2-15	Radix-2 DIF FFT	"	"		
26	24-2-15	Inverse FFT	"	"		
27	26-2-15	FFT for composite N	"	"		
28	27-2-15	Review of Z.T	IV	"		
29	2-3-15	Applications of Z.T	"	"		
30	2-3-15	Solution of diff eq of digital filters	"	"		
31	3-3-15	solution of diff eq of digital filters	"	"		
32	5-3-15	Block diag rep of LCC diff eq	"	"		
33	6-3-15	Basic Structures of IIR systems	"	"		
34	9-3-15	Basic structures of IIR systems	"	"		
35	9-3-15	Transposed forms	"	"		
36	10-3-15	Basic structures of FIR systems	"	"		
37	12-3-15	system function	"	"		
38	13-3-15	Analog filter (Butterworth) approximations	V	"		
39	16-3-15	Analog filter Butterworth approximations	"	"		
40	16-3-15	Chebyshev filter approx.	"	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
41	17-3-15	Chebyshev approx.	V	Black board		
42	19-3-15	Design of IIR digital filters from analog filters	"	"		
43	20-3-15	Design of IIR digital filters from analog filters	"	"		
44	23-3-15	Design of IIR digital filters from analog filters	"	"		
45	23-3-15	Analog – Digital transformations	"	"		
46	26-3-15	Analog – Digital transformations	"	"		
47	26-3-15	Characteristics of FIR digital filters	VI	"		
48	27-3-15	Freq response	"	"		
49	30-3-15	Design of FIR filters using Rect. window	"	"		
50	30-3-15	Triangular window	"	"		
51	31-3-15	Hamming window	"	"		
52	2-4-15	Hanning window	"	"		
53	3-4-15	Freq Sampling tech.	"	"		
54	6-4-15	Freq Sampling tech.	"	"		
55	6-4-15	Comparision of IIR & FIR filters	"	"		
56	7-4-15	Decimation	VII	"		
57	9-4-15	Decimation	"	"		
58	10-4-15	Interpolation	"	"		
59	13-4-15	Interpolation	"	"		
60	13-4-15	Problems on decimation & interpolation	"	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
61	14-4-15	Sampling rate conversion	VIII	Black board		
62	16-4-15	Sampling rate conversion	"	"		
63	17-4-15	Implementation of Sampling rate Conv.	"	"		
64	20-4-15	Implementation of Sampling rate Conv.	"	"		
65	20-4-15	Problems	"	"		
66	21-4-15	Problems	"	"		
67	23-4-15	Introduction to programable DSPs	VIII	PPT		
68	24-4-15	MAC	"	"		
69	27-4-15	Modified bus structures	"	"		
70	27-4-15	memory accen schemes in DSPs	"	"		
71	28-4-15	multiple accers memory multiport memory	"	"		
72	30-4-15	VLSI architecture, pipe lining	"	"		
73	1-5-15	Special addressing modes on-chip peripherals	"	"		
74	4-5-15	Arc of TMS 320C5X- introduction, bus structure	"	"		
75	4-5-15	central ALU, Auxiliary reg, index Reg, compare reg, Block move add rg, PLU,	"	"		
76	5-5-15	memory mapped registers PC, flags, on-chip reg, on-chip peripherals	"	"		
77	7-5-15	Revision				
78	8-5-15	Revision				

Staff