Faculty Achievements:2023-24

S.No	Name of the Faculty	Achievement
1	Dr.B.Rama Rao	Organised and acted as coordinator for FDP on "Recent trends
		in embedded systems and IOT's" on 18 th to 22 nd September-
	D G G G G G II	2023 in association with NITTTR-Chandigarh.
2	Dr.G.S.S.S.S.K.mohan	Organised and acted as co-coordinator for FDP on "Recent
		trends in embedded systems and IOT's" on 18 th to 22 nd
2	D D D	September-2023 in association with NITTR-Chandigarh.
3	Dr.B.Rama Rao	Organised and acted as coordinator for FDP on "Speech signal
		processing using Machine Learning algorithms" on 12 th to 17th
4	E Javo	February-2024 sponsored by ATAL Academy
4	E.Jaya	Organised and acted as co-coordinator for FDP on "Speech signal processing using Machine Learning algorithms" on 12 th
		to 17 th Febraury-2024 sponsored by ATAL Academy
5	Dr.P.Sirish kumar	Published a book titled "Digital logic design"
6	Dr.P.Kameswara Rao	Published a book titled "Digital logic design"
7	Dr.M.N.V.S.S.Kumar	Published a book titled "Digital logic design"
8	Dr.M.Jaya Manmadha	Published a book titled "Embedded systems"
	Rao	Tuonshed a book titled Embedded systems
9	Dr.V.Lokesh Raju	Organised and acted as co-coordinator for FDP on "NAAC and
	21,	Internal Quality Assurance" on 11 th to 15 th September-2023 in
		association with NITTTR-Chandigarh.
10	Dr.D.H.H.Santhosh	NPTEL Topper Elite+1% in Introduction to Machine Learning
11	Dr.D.H.H.Santhosh	NPTEL Topper Elite+2% in Introduction to Machine Learning
12	Dr.D.H.H.Santhosh	NPTEL Topper Elite+5% in Programming in Java
13	Dr.D.H.H.Santhosh	NDTEL Towns Eller 20/ 1/2 DDMC
13	DI.D.H.H.Sallulosli	NPTEL Topper Elite+2% in DBMS
14	Dr.D.H.H.Santhosh	NPTEL Topper Elite+5% in Data Science for Engineers
15	Dr.D.H.H.Santhosh	NPTEL Topper Elite+2% in Computer vision
16	Dr.D.H.H.Santhosh	NPTEL Domain scholar in Data Science
17	Dr.D.H.H.Santhosh	NPTEL Motivated learner in ML
18	E.Jaya	NPTEL Topper Elite+5% in DL
19	E.Jaya	NPTEL Topper Elite+5% in Introduction to Machine Learning
20	M.Chaitanya Kumar	NPTEL Topper Elite+2% in Digital circuits