

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
01	04-01-16	Microprocessor, 8086	01	BA		
02	05-01-16	Introduction, archi	4	4		
03	06-01-16	Architecture, register	4	4		
04	07-01-16	Organization, mem	4	4		
05	08-01-16	Organization	4	4		
06	11-01-16	Signal description	4	4		
07	15-01-16	and pin diagram	4	4		
08	20-01-16	addressing modes	4	4		
09	21-01-16	Assembler directives	4	4		
10	21-01-16	Procedures, macros	4	4		
11	22-01-16	Timing diagram	4	4		
12	25-01-16	Overview of 8086	4	4		
13	27-01-16					
14	28-01-16					
15	28-01-16	Assembly language	1P	4		
16	08-02-16					
17	10-02-16	Instruction set	4	4		
18	11-02-16	Machine language	4	4		
19	11-02-16	Instruction format	4	4		
20	15-02-16	Instruction set	4	4		
21	2-03-16	Assembly language	4	4		
22	04-03-16	Programs, inter	4	4		
23	04-03-16	direction to stack	4	4		

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24	07-03-16	Static structure,	11	NA		
25	9-03-16	classification of interrupts, interrupt	4	4		
26	11-03-16	Service routine and interrupt	4	4		
27	11-03-16	Vector table	4	4		
		<u>Microcontrollers</u>	12			
48	16-04-16	introduction, arc	4	4		
49	20-04-16	Structure, signal	4	4		
50	22-04-16	description, pin	4	4		
51	22-04-16	diagram, rep.	4	4		
52	25-04-16	stack set, memory	4	4		
		Segmentation, port	4	4		
53	27-04-16	16/18 I/O ports, interrupts and	4	4		
54	29-04-16	addressing modes of 8051	4	4		
55	29-04-16	Review and features of plc	4	4		
		microcontrollers	4	4		

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		Micro (P2C 16 C6X/7X) 01/11 13/1				
56	02-05-16	Per file selection	4	4		
		register, interr	4	4		
		upt in P2C	4	4		
		16C 61/71.	4	4		
57	04-05-16	Comparison of p2c	4	4		
		and flash code	4	4		
		Next	4	9		
		<u>Assembly language</u>	13	13		
		<u>Programming of</u>	4	4		
		<u>8051</u> Structure	4	4		
		of assembly	4	4		
		language - asse	4	4		
		mbler directives	4	4		
		different addre	4	4		
		8051 modes of	4	4		
		8051 - assembly	4	4		
		language program	4	4		
		and time delay	4	4		
		calculations	4	4		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
		<u>Interfacing units</u>	<u>IV</u>	<u>BB</u>		
36	28-03-16	8256/8251:	u	u		
		programmable inte	u	u		
		rupt controller	u	u		
		(8259A) - Progr-	u	u		
37	30-03-16	interrupt peripheral	u	u		
		interface (8255)	u	u		
38	01-04-16	modes of operation	u	u		
		of 8255 - DMA	u	u		
39	01-04-16	Controller (8257)	u	u		
40	04-04-16	Key board / display	u	u		
		controller	u	u		
41	06-04-16	(8255) - programme	u	u		
42	08-04-16	Communication	u	u		
43	08-04-16	Ctrn interface	u	u		
44	11-04-16	USART (8251)	u	u		
45	13-04-16	programming	u	u		
46	15-04-16	u	u	u		
47	15-04-16	u	u	u		
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