

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	7/1/16	Introduction to 8086	I	Class room teaching		
2	8/1/16	organiser Memory	I	CAT		
3	9/1/16	Signal description	I	"		
4	11/1/16	address supply	I	"		
5	14/1/16	pin diagram	I	"		
6	20/1/16	addressi Mode	I	"		
7	21/1/16	assembly language program	I	"		
8	21/1/16	Procedure, macro	I	"		
9	25/1/16	Timing diagram	I	"		
10	28/1/16	Some more program on 8086	II	"		
11	8/2/16	— do —	II	"		
12	19/2/16	Machine language	II	"		
13	14/2/16	Instruction format	II	"		
14	15/2/16	Instruction set	II	"		
15	22/2/16	ALU	II	"		
16	24/2/16	Program Intra	II	"		
17	26/2/16	direct to Stack	II	"		
18	29/2/16	Stack structure	II	"		
19	27/2/16	classification of Interrupts	II	"		
20	2/3/16	Service routine	III	"		

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1	3/2/16	Vector table	II	LRT		
2	5/3/16	Advanced Micro processing Introduction	IV	4		
23	7/3/16	Architecture register organization	II	4		
24	9/3	Signal description	II	4		
25	11/3	Advanced Architecture of 586	III	4		
26	14/3	Register organization	IV	4		
27	16/3	Signal description	IV	4		
28	17/3	Mode of operation real address	IV	4		
29	17/3	program (Programs)	IV	4		
30	21/3	Worked Mode of operation	IV	4		
31	24/3	do -	IV	4		
32	31/3	do -	IV	4		
33	4/4	Interfacing controller	IV	4		
34	6/4	PIC interfacing	IV	4		
35	8/4	do -	IV	4		
36	7/4	PPI interfacing	IV	4		
37	21/4	do -	IV	4		
38	11/4	DMA interfacing	IV	4		
39	18/4	do	IV	4		
40	22/4	programmable	IV	4		

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41	21/4					
42	24/4	8001 pm down structure	<u>IV</u>	PPT		
43	23/4	Admini Media	<u>IV</u>	PPT		
44	23/4	advanced tech	<u>V</u>	4		
45	21/4	Intemp of force	<u>IV</u>	4		
46	25/4	Alu foot	<u>V</u>	4		
47	28/4	Alu pugn	<u>V</u>	4		
48	29/4	- di -	<u>V</u>	4		8/1/02