

**ADITYA INSTITUTE OF TECHNOLOGY AND MANAGEMENT
(AN AUTONOMOUS INSTITUTION)**

EXAMINATION BRANCH

I M. TECH - II SEMESTER – (AR13 REGULATION) 2013 BATCH (AUTONOMOUS)

II MID EXAMINATIONS, JUNE-2014

TIME TABLE

DATE: 30-05-2014

Examination Timings 02.00 PM to 04.00 PM

Date & Day of Examination	TE	DECS	IT	PEED	VLSI System Design	CSE
23-06-2014 (MONDAY)	Fuels, Combustion & Environment (13MTE1011)	DSP Processors and architecture (13MVL1009)	Image Processing (13MIT1007)	Switched mode power converters (13MPE1009)	DSP Processors and architecture (13MVL1009)	Data Warehousing And Data Mining (13MCS1007)
24-06-2014 (TUESDAY)	Energy Management (13MTE1012)	Wireless Communication & Networks (13MDE1004)	Parallel Computing Architecture (13MIT1008)	Power electronic control of AC drives (13MPE1010)	Algorithms for VLSI Design Automation (13MVL1010)	Computer Networks (13MCS1008)
25-06-2014 (WEDNESDAY)	Finite Element Analysis (13MTE1013)	Optical Communication (13MDE1005)	Machine Learning (13MIT1009)	Flexible AC Transmission systems (13MPE1011)	Low Power VLSI Design (13MVL1011)	Object Oriented Analysis & Design (13MCS1009)
26-06-2014 (THURSDAY)	Computational Fluid Dynamics (13MTE1014)	Coding Theory & Practice (13MDE1006)	Data Mining & Knowledge Discovery (13MIT1010)	Advanced digital signal processing and applications (13MPE1012)	Design of Fault Tolerant Systems (13MVL1012)	Web Technologies (13MCS1010)
27-06-2014 (FRIDAY)	Thermal & Nuclear Power Plants (13MTE1017)	System Modeling & Simulation (13MVL1013)	Information Security (13MIT1013)	Intelligent control (13MPE1014)	System Modeling & Simulation (13MVL1013)	Mobile Computing (13MCS1011)
28-06-2014 (SATURDAY)	Refrigeration & Air Conditioning (13MTE1019)	Image and Video Processing (13MDE1008)	Bio-Informatics (13MIT1016)	Power Quality Management (13MPE1015)	CPLD & FPGA Architecture and Applications (13MVL1015)	Image Processing (13MCS1018)

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KB Madhu Sahu

PRINCIPAL