

**ADITYA INSTITUTE OF TECHNOLOGY AND MANAGEMENT
(AN AUTONOMOUS INSTITUTION)**

EXAMINATION BRANCH

I M. TECH - II SEMESTER – (AR16 REGULATION) 2017 BATCH

MID-I EXAMINATIONS, APRIL-2018

TIME TABLE

Examination Timings 02.00 PM to 04.00 PM

Date & Day of Examination	SE	TE	DECS	VLSI System Design	PED	CSE
23-04-2018 (MONDAY)	Finite Element Method in Structural Engineering (16MSE1011)	Fuels, Combustion & Environment (16MTE1011)	Spread Spectrum Communications (16MDE1005)	Mixed Signal IC design (16MVL1009)	Switched Mode Power Converters (16MPE1009)	Data Mining and Knowledge Discovery (16MCS1010)
24-04-2018 (TUESDAY)	Computer Applications and CAD (16MSE1012)	Energy Management (16MTE1012)	Detection and Estimation of Signals (16MDE1006)	Algorithms for VLSI Design Automation (16MVL1010)	Power Electronic Control of AC Drives (16MPE1010)	Software Engineering (16MCS1011)
25-04-2018 (WEDNESDAY)	Stability of Structures (16MSE1013)	Finite Element Analysis (16MTE1013)	Wireless Communication & Networks (16MDE1007)	Low Power VLSI Design (16MVL1011)	Flexible AC Transmission Systems (16MPE1011)	Object Oriented Analysis & Design (16MCS1012)
26-04-2018 (THURSDAY)	Structural Dynamics & Earthquake Resistant Design (16MSE1014)	Computational Fluid Dynamics (16MTE1014)	Coding Theory and Techniques (16MDE1008)	Design of Fault Tolerant Systems (16MVL1012)	Advanced Digital Signal Processing and Applications (16MPE1012)	Web Technologies (16MCS1013)
27-04-2018 (FRIDAY)	Elective - III Prestressed Concrete (16MSE1015)	Elective – III Thermal & Nuclear Power Plants (16MTE1017)	Elective - III System Modeling and Simulation (16MVL1014)	Elective - III System Modeling & Simulation (16MVL1014)	Elective - III Analysis of Dynamic Systems (16MPE1013)	Elective - II Mobile Computing (16MCS1014)
28-04-2018 (SATURDAY)	Elective – IV Design of Substructures (16MSE1020)	Elective – IV Refrigeration & Air-Conditioning (16MTE1019)	Elective – IV Adaptive Signal Processing (16MDE1009)	Elective – IV CPLD and FPGA Architecture and Applications (16MVL1015)	Elective – IV Power Quality Management (16MPE1015)	Elective – III Digital Image Processing (16MCS1021)

Copy to: Director / Principal / Controller of the Examinations / HODs / Officer I/c, Examinations

09-04-2018

PRINCIPAL