

**ADITYA INSTITUTE OF TECHNOLOGY AND MANAGEMENT
(AN AUTONOMOUS INSTITUTION)**

EXAMINATION BRANCH

I M. TECH - II SEMESTER – (AR16 REGULATION) 2016 ADMITTED BATCH ONLY

REGULAR EXAMINATIONS, JULY-2017

TIME TABLE

Examination Timings 10.00 AM to 01.00 PM

Date & Day of Examination	SE	TE	DECS	VLSI System Design	PED	CSE
03-07-2017 (MONDAY)	Finite Element Method in Structural Engineering (16MSE1011)	Fuels, Combustion & Environment (16MTE1011)	Spread Spectrum Communications (16MDE1005)	Mixed Signal IC Design (16MVL1009)	Switched Mode Power Converters (16MPE1009)	Data Mining and Knowledge Discovery (16MCS1010)
05-07-2017 (WEDNESDAY)	Computer Applications and CAD (16MSE1012)	Energy Management (16MTE1012)	Detection and Estimation of Signals (16MDE1006)	Algorithms for VLSI Design Automation (16MVL1010)	Power Electronic Control of AC Drives (16MPE1010)	Software Engineering (16MCS1011)
07-07-2017 (FRIDAY)	Stability of Structures (16MSE1013)	Finite Element Analysis (16MTE1013)	Wireless Communication & Networks (16MDE1007)	Low Power VLSI Design (16MVL1011)	Flexible AC Transmission Systems (16MPE1011)	Object Oriented Analysis & Design (16MCS1012)
10-07-2017 (MONDAY)	Structural Dynamics & Earthquake Resistant Design (16MSE1014)	Computational Fluid Dynamics (16MTE1014)	Coding Theory and Techniques (16MDE1008)	Design of Fault Tolerant Systems (16MVL1012)	Advanced Digital Signal Processing and Applications (16MPE1012)	Web Technologies (16MCS1013)
12-07-2017 (WEDNESDAY)	Elective - III Prestressed Concrete (16MSE1015)	Elective – III Thermal & Nuclear Power Plants (16MTE1017)	Elective - III System Modeling and Simulation (16MVL1014)	Elective - III System Modeling & Simulation (16MVL1014)	Elective - III Analysis of Dynamic Systems (16MPE1013)	Elective - II Cryptography & Network Security (16MCS1015)
14-07-2017 (FRIDAY)	Elective – IV Design of Substructures (16MSE1020)	Elective – IV Refrigeration & Air-Conditioning (16MTE1019)	Elective – IV Adaptive Signal Processing (16MDE1009)	Elective – IV CPLD and FPGA Architecture and Applications (16MVL1015)	Elective – IV Power Quality Management (16MPE1015)	Elective – III Digital Image Processing (16MCS1021)

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08-06-2017

PRINCIPAL

**ADITYA INSTITUTE OF TECHNOLOGY AND MANAGEMENT
(AN AUTONOMOUS INSTITUTION)**

EXAMINATION BRANCH

I M. TECH - II SEMESTER – (AR13 REGULATION)

SUPPLEMENTARY EXAMINATIONS, JULY-2017

TIME TABLE

Examination Timings 10.00 AM to 01.00 PM

Date & Day of Examination	SE	TE	DECS	PEED	VLSI System Design	CSE
03-07-2017 (MONDAY)	Finite Element Method (13MSE1011)	Fuels, Combustion & Environment (13MTE1011)	DSP Processors and Architecture (13MVL1009)	Switched Mode Power Converters (13MPE1009)	DSP Processors and Architecture (13MVL1009)	Data Warehousing and Data Mining (13MCS1007)
05-07-2017 (WEDNESDAY)	Computer Applications and CAD (13MSE1012)	Energy Management (13MTE1012)	Optical Communication (13MDE1005)	Power Electronic Control of AC Drives (13MPE1010)	Algorithms for VLSI Design Automation (13MVL1010)	Computer Networks (13MCS1008)
07-07-2017 (FRIDAY)	Stability of Structures (13MSE1013)	Finite Element Analysis (13MTE1013)	Wireless Communication & Networks (13MDE1004)	Flexible AC Transmission Systems (13MPE1011)	Low Power VLSI Design (13MVL1011)	Object Oriented Analysis & Design (13MCS1009)
10-07-2017 (MONDAY)	Structural Dynamics and Earthquake Resistant Design (13MSE1014)	Computational Fluid Dynamics (13MTE1014)	Coding Theory & Practice (13MDE1006)	Advanced Digital Signal Processing And Applications (13MPE1012)	Design of Fault Tolerant Systems (13MVL1012)	Web Technologies (13MCS1010)
12-07-2017 (WEDNESDAY)	Pre-Stressed Concrete (13MSE1015)	Thermal & Nuclear Power Plants (13MTE1017)	System Modeling & Simulation (13MVL1013)	Analysis of Dynamic Systems (13MPE1013)	System Modeling & Simulation (13MVL1013)	Mobile Computing (13MCS1011)
				Intelligent Control (13MPE1014)		Network Security and Cryptography (13MCS1016)
14-07-2017 (FRIDAY)	Design of Substructures (13MSE1020)	Refrigeration & Air Conditioning (13MTE1019)	Image and Video Processing (13MDE1008)	Power Quality Management (13MPE1015)	CPLD & FPGA Architecture and Applications (13MVL1015)	Image Processing (13MCS1018)

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